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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	Not Yet Assigned 10/798347
				Filing Date	March 12, 21004
				First Named Inventor	Giuseppe Rossi
				Art Unit	N/A 2858
				Examiner Name	Not Yet Assigned A. Deb
Sheet	1	of	1	Attorney Docket Number	M4065.0642/P642-A

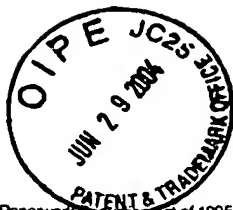
U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
IAD/ ↓ ↓ ↓ ↓ ↓ ↓	A**	4,862,237	08-29-1989	Morozumi	
	B**	5,886,353	03-23-1999	Spivey et al.	
	C**	5,929,689	07-27-1999	Wall	
	D**	5,953,060	09-14-1999	Dierickx	
	E**	6,101,287	08-08-2000	Corum et al.	
	F**	6,330,345	12-11-2001	Russo et al.	
	G**	6,418,241	07-09-2002	Schreiner	

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		Country Code ² -Number ⁴ -Kind Code ⁵ (if known)				

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
IAD/	H**	AN03: Guide to Image Quality and Pixel Correction Methods, Rad-Icon Imaging Corp,		
↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	I**	ANTON BAKKER and JOHAN H. HUIJSING; Micropower CMOS Temperature Sensor with Digital Output, IEEE Journal of Solid-State Circuits, Vol. 31, No. 7, July 1996, pp. 933-937.		
	J**	KARIM ARABI and BOZENA KAMINSKA, Built-In Temperature Sensors for On-line Thermal Monitoring of Microelectronic Structures, Proceedings of the 1997 International Conference on Computer Design (ICCD '97), pp. 462-467.		
	K**	LOUIS LUH, JOHN CHOMA, JR., JEFFREY DRAPER, HERMING CHIUH, A High-Speed CMOS On-Chip Temperature Sensor, Proceedings of the European Solid State Circuits Conference, September 1999, pp. 290-293.		
	L**	V. SZEKELY, M. RENCZ and B. COURTOIS, Integrating On-chip Temperature Sensors into DFT Schemes and BIST Architectures, Proceedings of the 15th IEEE VLSI Test Symposium, IEEE, (1997), pp. 440-445.		
	M**	M. LOOSE, K. MEIER, J. SCHEMMEL, Self-calibrating logarithmic CMOS image sensor with single chip camera functionality, Contribution to IEEE CCD & AIS workshop, Karuizawa, Japan (1999) R27.		
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	P**	C. C. LUI and C. H. MASTRANGELO, CMOS Uncooled Heat-Balancing Infrared Imager, IEEE Journal of Solid-State Circuits, April 2000, pp. 1-9.		

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PTO/SB/08a/b (08-03)
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	AB**	US-5,452,001	09-19-1995	Hosier et al.	
	AC**	US-2003/0202111-A1	10-30-2003	Park	
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/Anjan Deb/ (04/16/2007)